#### ELEC4410

## **Control System Design**

#### Lecture 8: Design Limitations in Control

School of Electrical Engineering and Computer Science The University of Newcastle



Sensors & Actuators.



- Sensors & Actuators.
- Disturbances.



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- Disturbances.
- Model deficiencies.



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- Structural issues.



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Reference: Control System Design, Goodwin, Graebe & Salgado.





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Any error, or significant defect, in the measurement system will have a significant impact on performance.



> The effect of measurement noise in the nominal loop is given by

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Measurement noise is typically dominated by high frequencies.





# Conclusion

Measurement noise usually sets an upper limit on the bandwidth of the loop.



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  - maximal movement



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$$U(s) = S_{uo}(s)(R(s) - D_o(s)) \quad \text{where} \quad S_{uo}(s) \triangleq \frac{T_o(s)}{G_o(s)}$$



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If the loop bandwidth is much larger than that of the open loop model G<sub>o</sub>(s), then the transfer function S<sub>uo</sub>(s) will significantly enhance the high frequency components in R(s) and D<sub>o</sub>(s).



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$$G_o(s) = \frac{10}{(s+10)(s+1)}$$
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- Note that the plant and the closed loop bandwidths have a ratio of approximately 10:1.
- This will be reflected in large control sensitivity,  $|S_{u0}(j\omega)|$ , at high frequencies.
- This will yield a large initial control response in the presence of high frequency reference signals or disturbances.





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- The input signal resulting from a unit step disturbance is shown on the right hand plot.
- Note that the initial value of the input is approximately ten times the

size of the steady state input needed to cancel the input.



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Hence, the rate of change of the input is given by

$$sU(s) = S_{uo}(s)[sR(s) - sD_o(s)] = \frac{T_o(s)}{G_o(s)}[sR(s) - sD_o(s)].$$



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Thus, if the bandwidth of the closed loop is much larger than that of the plant dynamics, then the rate of change of the input signal will be large for fast changes in r(t) and d<sub>o</sub>(t).

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# Conclusion

To avoid actuator saturation or slew rate problems, it will generally be necessary to place an upper limit on the closed loop bandwidth.



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- The manifestations of the problem are usually a self sustaining oscillation produced as the actuator goes through a cycle of sticking, moving, sticking and so on.
- The oscillation frequency is typically at or near the frequency where the loop phase shift is 180°.


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- Assume that the input and output disturbances have significant energy only in the frequency bands  $B_{wi}$  and  $B_{wo}$  respectively.
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- Assume that the input and output disturbances have significant energy only in the frequency bands  $B_{wi}$  and  $B_{wo}$  respectively.
- Then, it is clearly desirable to have small values for  $|S_o(j\omega)|$  and  $|S_{io}(j\omega)|$  in  $B_{wi}$  and  $B_{wo}$  respectively.
- Since G(s) is fixed, this can only be achieved provided that S<sub>o</sub>(jω) ≈ 0, and hence T<sub>o</sub>(jω) ≈ 1 in the frequency band encompassing the union of B<sub>wi</sub> and B<sub>wo</sub>.





# Conclusion

To achieve acceptable performance in the presence of disturbances, it will generally be necessary to place a lower bound on the closed loop bandwidth.



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- This implies that  $|G_{\Delta}(j\omega)|$  will become increasingly significant with rising frequency.



# Conclusion

To achieve acceptable performance in the presence of model errors, it will generally be desirable to place an upper limit on the closed loop bandwidth.



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  - Open loop zeros
  - Open loop poles



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To achieve this ideal result requires use of a Smith Predictor plus an ideal controller.



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$$T_o^*(s) = e^{-s\tau}$$

- This has gain 1 at all frequencies.
- Hence high frequency model errors will lead to instability unless the bandwidth is limited.
- Errors in the delay are particularly troublesome.



# Conclusion

- 1. Delays limit disturbance rejection by requiring that a delay occur before the disturbance can be cancelled. This is reflected in the ideal sensitivity  $S_{o}^{*}(s)$
- 2. Delays further limit the achievable bandwidth due to the impact of model errors.



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  - The answer is probably yes if the system model (especially the delay) is accurately known.
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- We have seen above that delays (where the response does not move for a given period) represent a very important source of structural limitations in control design.
- We might then conjecture that non-minimum phase behaviour (where the response initially goes in the wrong direction) may present even harder challenges to control system design?



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- We shall see that open loop poles and zeros have a dramatic (and predictable) effect on closed loop performance.
- We begin by examining the so-called interpolation constraints which show how open loop poles and zeros are reflected in the poles and zeros of the various closed loop sensitivity functions.



• Recall that the relevant nominal sensitivity functions for a nominal plant  $G_o(s) = \frac{B_o(s)}{A_o(s)}$  and a given unity feedback controller  $C(s) = \frac{P(s)}{L(s)}$  are:

$$T_{o}(s) = \frac{G_{o}(s)C(s)}{1 + G_{o}(s)C(s)} = \frac{B_{o}(s)P(s)}{A_{o}(s)L(s) + B_{o}(s)P(s)}$$
$$S_{o}(s) = \frac{1}{1 + G_{o}(s)C(s)} = \frac{A_{o}(s)L(s)}{A_{o}(s)L(s) + B_{o}(s)P(s)}$$
$$S_{io}(s) = \frac{G_{o}(s)}{1 + G_{o}(s)C(s)} = \frac{B_{o}(s)L(s)}{A_{o}(s)L(s) + B_{o}(s)P(s)}$$
$$S_{uo}(s) = \frac{C(s)}{1 + G_{o}(s)C(s)} = \frac{A_{o}(s)P(s)}{A_{o}(s)L(s) + B_{o}(s)P(s)}$$


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- **4.** The nominal complementary sensitivity  $T_o(s)$  is equal to one at all uncancelled poles of  $G_o(s)$ . (This follows from (**3**.) and the identity  $S_o(s) + T_o(s) = 1$ ).



**Lemma.** We assume that the plant is controlled in a one d.o.f. configuration and that the open loop plant and controller satisfy:

$$A_o(s)L(s) = s^i (A_o(s)L(s))' \quad i \ge 1$$
$$\lim_{s \to 0} (A_o(s)L(s))' = c_0 \neq 0$$
$$\lim_{s \to 0} (B_o(s)P(s)) = c_1 \neq 0$$

i.e. the plant-controller combination has *i* poles at the origin. Then, for a step output disturbance or step set point, the control error, e(t), satisfies

$$\lim_{t \to \infty} e(t) = 0 \quad \forall i \ge 1$$
$$\int_{0}^{\infty} e(t)dt = 0 \quad \forall i \ge 2$$



**Lemma.** (cont.) Also, for a negative unit ramp output disturbance or a positive unit ramp reference, the control error, e(t), satisfies

$$\lim_{t \to \infty} e(t) = \frac{c_0}{c_1} \quad \text{for } i = 1$$
$$\lim_{t \to \infty} e(t) = 0 \quad \forall i \ge 2$$
$$\int_0^\infty e(t)dt = 0 \quad \forall i \ge 3$$



#### **Example: Equal Area Result**

Say  $G_o(s)C(s)$  contains a double integrator  $\Rightarrow S_o(s)$  has a double zero at s = 0.



The above holds for a one d.o.f. feedback control system. Overshoot can actually be avoided if the architecture is changed to a two-degree-of-freedom control system.



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- However, we then see that the error to a step reference change must satisfy

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- This, in turn, implies that the error must change sign, i.e. overshoot must occur.
- Thus it is impossible to have zero steady state error to ramp type disturbances together with no overshoot to a step reference.

In the case of input disturbances, the numerator of  $S_{io}(s)$  is  $B_o(s)L(s)$  rather than  $A_o(s)L(s)$  as was the case for  $S_o(s)$ . This implies that integration in the plant does not impact on the steady state compensation of input disturbances. Thus we need to modify the previous Lemma:



In the case of input disturbances, the numerator of  $S_{io}(s)$  is  $B_o(s)L(s)$  rather than  $A_o(s)L(s)$  as was the case for  $S_o(s)$ . This implies that *integration in the plant does not impact on the steady state compensation of input disturbances*. Thus we need to modify the previous Lemma: Lemma. Assume that the controller satisfies:

$$L(s) = s^{i}(L(s))' \quad i \ge 1$$
$$\lim_{s \to 0} (L(s))' = l_{i} \ne 0$$
$$\lim_{s \to 0} (P(s)) = p_{0} \ne 0$$

the controller alone has *i* poles at the origin. Then, for a step input disturbance, the control error, e(t), satisfies

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\lim_{t \to \infty} e(t) = 0 \quad \forall i \ge 1\int_{-\infty}^{\infty} e(t)dt = 0 \quad \forall i \ge 2
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**Lemma.** (cont.) Also, for a negative unit ramp input disturbance, the control error, e(t), satisfies

$$\lim_{t \to \infty} e(t) = \frac{l_i}{p_0} \quad \text{for} \quad i = 1$$
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- The results above depend upon the zeros of the various sensitivity functions at the origin.
- However, it turns out that zeros in the right half plane have an even more dramatic effect on achievable transient performances of feedback loops.
- Hence, we shall develop a series of integral constraints that apply to the transient response of feedback systems having various combinations of open loop poles and zeros.



**Lemma.** Consider a feedback control loop having stable closed loop poles located to the left of  $-\alpha$  for some  $\alpha > 0$ . Also assume that the controller has at least one pole at the origin. Then, for an uncancelled plant zero  $z_0$  or an uncancelled plant pole  $\eta_0$  to the right of the closed loop poles, i.e. satisfying  $\Re\{z_0\} > -\alpha$  or  $\Re\{\eta_0\} > -\alpha$  respectively, we have



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(i) For a positive unit reference step or a negative unit step output disturbance, we have

$$\int_{0}^{\infty} e(t)e^{-z_{0}t}dt = \frac{1}{z_{0}}$$
$$\int_{0}^{\infty} e(t)e^{-\eta_{0}t}dt = 0$$



Lemma. (cont.)

(ii) For a positive unit step reference and for  $z_0$  in the right half plane, we have

$$\int_{0}^{\infty} y(t) e^{-z_0 t} dt = 0$$



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(iii) For a negative unit step input disturbance, we have

$$\int_{0}^{\infty} e(t)e^{-z_{0}t}dt = 0$$
$$\int_{0}^{\infty} e(t)e^{-\eta_{0}t}dt = \frac{L(\eta_{0})}{\eta_{0}P(\eta_{0})}$$



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(ii) For a positive unit step reference and for  $z_0$  in the right half plane, we have

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(iii) For a negative unit step input disturbance, we have

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The above integral constraints show that (irrespective of how the closed loop control system is designed) the closed loop performance

is constrained in various ways.

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A real stable (LHP) zero to the right of all closed loop poles produces overshoot in the step response.



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- A real unstable (RHP) zero always produces undershoot in the step response. The amount of undershoot grows as the zero approaches the origin.



- A real stable (LHP) zero to the right of all closed loop poles produces overshoot in the step response.
- A real unstable (RHP) zero always produces undershoot in the step response. The amount of undershoot grows as the zero approaches the origin.
- Any real open loop pole to the right of all closed loop poles will produce overshoot in a one d.o.f. control architecture.



#### Conclusion

To avoid poor closed loop transient performance:

- 1. The bandwidth should in practice be set less than the smallest non minimum phase zero.
- 2. It is advisable to set the closed loop bandwidth greater than the real part of any unstable pole.



#### Example: Effect of different locations of poles and zeros in the loop performance.

Consider a nominal plant model given by

$$G_o(s) = \frac{s - z_p}{s(s - p_p)}.$$

The closed loop poles were assigned to  $\{-1, -1, -1\}$ . Then, the general controller structure is given by

$$C(s) = K_c \frac{s - z_c}{s - p_c}.$$

Five different cases are considered.



#### Example: (cont.)

	Case 1	Case 2	Case 3	Case 4	Case 5
	$p_{\rho} = -0.2$	$p_p = -0.5$	$p_{p} = -0.5$	$p_{p} = 0.2$	$p_p = 0.5$
	$z_{p} = -0.5$	$z_{p} = -0.1$	$z_{p} = 0.5$	$z_{p} = 0.5$	$z_{\rho} = 0.2$
K <sub>c</sub>	1.47	20.63	-3.75	-18.8	32.5
p <sub>c</sub>	-1.33	18.13	-6.25	-22.0	29.0
Z <sub>C</sub>	-1.36	-0.48	-0.53	-0.11	0.15



**Example: (cont.)** The different designs were tested with a unit step reference and, in every case, the plant output was observed.



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- Case 4 (Unstable zero, small unstable pole) We first observe significant undershoot due to the RHP zero. We also observe a significant overshoot due to the unstable pole.
- Case 5 (Small unstable zero, large unstable pole) Here the undershoot is produced by the RHP zero and the overshoot by RHP pole. In this case the overshoot is significantly larger than in Case 4, due to the fact that the unstable pole is further into the RHP.

#### **Structural Limitations**

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  - open loop plant poles



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- This suggests that the one to overcome these limitations is to consider changing the basic architecture of the problem.
- > We only examined the effects with respect to a one d.o.f. architecture.



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- Note that they remain a difficulty inside the loop and thus contribute to design trade-offs regarding robustness, disturbance rejection, etc.



#### Structural Limitations - Example

Effect of Two Degree of Freedom Architecture on Closed Loop Response with PI Control.

Consider the feedback control of plant with a PI controller:

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- Closed loop poles are (-1; -1) and the controller has a zero at (-0.5).
- If we prefilter the reference by  $H(s) = \frac{1}{2s+1}$ , then no overshoot occurs in response to a step change in the reference signal.





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